H4Z

AMD FPGA and HAILO ASIC Communication Library



Software product



$\ensuremath{\mathsf{H4Z}}$ is a software integration library for AMD FPGAs and HAILO ASIC

Useful for:

- Work unloading for AI tasks
- Enabling AI tasks implementation on FPGAs that don't support DPU
- Rapid prototyping / fast development
- Complex or heterogeneous architectures





Technical brief

Toolchain used:

- Vivado 2021.1
- Petalinux 2021.1
- Yocto project version Gatesgrath 3.2
- Linux kernel 5.10
- FSBL 2021.1
- U-Boot 2021.01

HAILO Drivers

- hailo firmware 4.8.1
- hailo pci 4.8.1
- hailo post processes 3.19.1
- hailortcli 4.8.1
- libgsthailo 4.8.1
- libgsthailotools 3.19.1
 - libhailort 4.8.1

Tappas app 3.19.1

VCU Drivers

- Kernel-module-vcu 1.0.0
- libvcu-xlnx 1.0.0
- libomxil-xlnx 1.0.0
- vcu firmware 1.0.0

AV Utilities

gstreamer 1.0-omx-1.16.3

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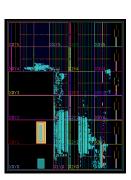
openCV 4

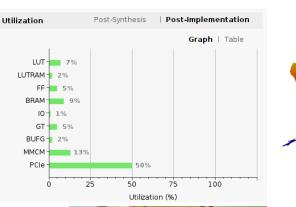




Resources usage

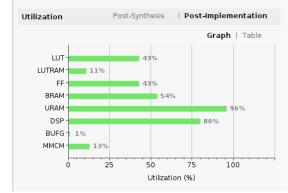
FPGA usage with FMC + VCU decoder on ZCU104 (distribute workload! The FPGA is quite free!)

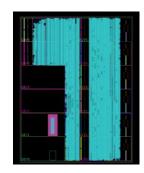






FPGA usage with 2 DPU on ZCU104 (all stuffed! There is very little space for custom design)





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You will receive only 1 installer file, with a one-tap wizard

- HAILO Installer Copyright @ MakarenaLabs srl rel v1.0:0 This installer will install Hailo required software and drivers. Do you agree? [Y/n]n 0k. Bye! root@xilinx-zcu104-2021_1:-# ./hailo_installer.sh HAILO Installer Copyright @ MakarenaLabs srl rol v1.0.0

rel v1.0.0 This installer will install Hailo required software and drivers. Do you agree? [Y/n]y

Complete!

Installation complete. Restart your board!

Easy deploy on your current working petalinux image

- based on RPM package
- No Petalinux and Vivado skills required
- ▷ No Petalinux rebuild image required
- No Vivado design required
- Possible Vivado integration on request



VCU Decoder (FPGA enabling + petalinux Drivers), just specify if you need H264 or H265 for your application
 HAILO driver (FPGA bindings + petalinux Drivers)

root@xilinx-zcu104-2021_1:~# lsmod				
Module	Size	Used by		
dmaproxy	16384	0		
al5d	16384	0		
allegro	36864	1 al5d		
hailo_pci	61440	0		
xlnx_vcu	20480	1 allegro		
mali	233472	0		
regmap_mmio	16384	1 xlnx_vcu		
uio_pdrv_genirq	16384	0		

root@xilinx-zcu104-2021_1:~# ls /dev/all*
/dev/allegroDecodeIP

root@xilinx-zcu104-2021_1:~# ls /dev/hailo*
/dev/hailo0
root@xilinx-zcu104-2021_1:~# hailortcli scan
Hailo PCIe Devices:



Integration with <u>TAPPAS HAILO</u> (below the cascade face detection + landmark example)

gst-launch-1.0 v4l2src device=/dev/video0 name=src 0 decodebin \ 🛛 📥 H264 compressed USB camera videoflip video-direction=horiz \ videoconvert gos=false n-threads=4 \ queue \ tee name=t hailomuxer name=hmux t. \ queue leaky=no max-size-buffers=3 max-size-bytes=0 max-size-time=0 \ hmux. t. \ videoscale gos=false n-threads=3 \ queue leaky=no max-size-buffers=3 max-size-bytes=0 max-size-time=0 \ hailonet net-name=joined lightface slim tddfa_mobilenet_v1/lightface_slim hef-path=/home/root/apps/cascading_networks/resources/joined_lightface_slim_tddfa_mobilenet_v1.hef_is-active=true_vdevice-key=1 \ queue leaky=no max-size-buffers=3 max-size-bytes=0 max-size-time=0 \ hailofilter so-path=/usr/lib/hailo-post-processes/libface detection post.so function-name=lightface gos=false \ queue leaky=no max-size-buffers=3 max-size-bytes=0 max-size-time=0 \ hmux, hmux, \ queue leaky=no max-size-buffers=3 max-size-bytes=0 max-size-time=0 \ hailocropper so-path=/usr/lib/hailo-post-processes/cropping algorithms/lib3ddfa.so function-name=create crops internal-offset=false name=cropper hailoaggregator name=agg cropper. \ queue leaky=no max-size-buffers=3 max-size-bytes=0 max-size-time=0 \ agg. cropper. \ queue leaky=no max-size-buffers=3 max-size-bytes=0 max-size-time=0 \ hailonet net-name=joined lightface slim tddfa mobilenet v1/tddfa mobilenet v1 hef-path=/home/root/apps/cascading networks/resources/joined lightface slim tddfa mobilenet v1.hef is-active=true vdevice-key=1 \ queue leaky=no max-size-buffers=3 max-size-bytes=0 max-size-time=0 \ hailofilter function-name=facial landmarks merged so-path=/usr/lib/hailo-post-processes/libfacial landmarks post.so qos=false \ queue leaky=no max-size-buffers=3 max-size-bytes=0 max-size-time=0 \ agg. agg. ∖ queue leaky=no max-size-buffers=3 max-size-bytes=0 max-size-time=0 \ hailooverlay gos=false \ queue leaky=no max-size-buffers=3 max-size-bytes=0 max-size-time=0 \ videoconvert \ kmssink bus-id=fd4a0000.display fullscreen-overlay=1



TAPPAS HAILO

gst=lambt-14 wilzerc derice/wey/dew nameserc_0 \
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Roadmap





	2023			
	Q1	Q2	Q3	Q4
1.0.0	Petalinux 2021.1– Hailo RT 4.8			
1.5.0		Petalinux 2022.2 – Hailo RT 4.8		
2.0.0			Petalinux 2023.1 – Hailo RT 4.13	
HAILO RT	4.12	4.13	4.14	4.15
AMD Tools	2022.2 – Yocto Honister	2023.1 – Yocto Ho	onister / Kirkstone	2023.2

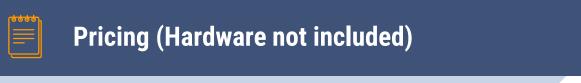




Next releases Release 1.0.0 Petalinux > 2021.1 Petalinux 2021.1 Support for Zynq7000 (zc702) ZynqMP (tested on zcu104) Support for KRIA K26 TAPPAS tested (h264 camera and MP4 file): Support for different development board (ZU1 – ZU9) Cascade (face detection + face Full TAPPAS support landmark) Detection (object detection) Landmark (face landmark)

What is the «best» desiderata?

Business model & Pricing







- One-tap installer*
- 1 month online support** (Mon-Fri 9 AM 6PM)
- No recurrent fee
- No device royalties
- Incremental discount 5% for new major release***

Manufacturer's Suggested Retail Price 15'000 € + VAT (if appliable)

* On-site test only with MakarenaLabs staff

** Installer support, no custom hardware/software support included

^{***} maximum discount 25% for consecutive purchases

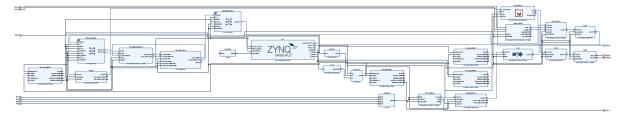


Consultancy services

Consultancy

- Custom software/firmware design support
 - We have a big expertise on AMD FPGA devices
- Petalinux packages integration (image build or RPM provider)
- Additional hardware integration
- Additional TAPPAS support

The pricing depends on the specific project



Example: EDDP-SPYN Motor control for ZynqMP with predictive maintenance <u>https://tinyurl.com/5n8xbhzy</u>



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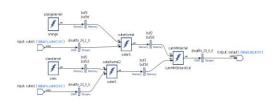
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Consultancy

-

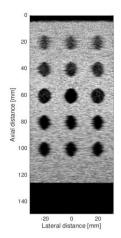
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Example:

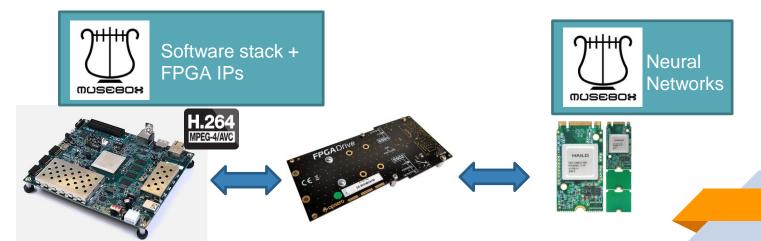
Medical imaging for Ultrasound on Versal VCK190 https://tinyurl.com/s4t4r6sy







- MuseBox is a framework for distributed and heterogenous Machine Learning applications
- Every ML tasks is deployable on HAILO chip (<u>https://musebox.it/solutions/</u>)
- Useful for developers
 - Useful for final product integration (<u>https://store.musebox.it/</u>)







THANKS!

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